



Analog IC Design with Low-Dropout Regulators, Second Edition (Electronics)

By Gabriel Rincon-Mora

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Analog IC Design with Low-Dropout Regulators, Second Edition (Electronics) By Gabriel Rincon-Mora

THE LATEST ANALOG IC DESIGN TECHNIQUES

Fully revised and expanded to meet the emerging demands of mixedsignal systems, *Analog IC Design with Low-Dropout Regulators*, Second Edition, teaches analog IC concepts and explains how to use them to design, analyze, and build linear low-dropout (LDO) regulator ICs with bipolar, CMOS, and biCMOS semiconductor process technologies. The book draws physical insight from topics presented and illustrates how to develop and evaluate analog ICs for today's expanding wireless and mobile markets. Practical examples and end-of-chapter review questions reinforce important concepts and techniques developed in this cutting-edge guide.

LEARN HOW TO:

- Evaluate power-supply systems
- Predict and specify how linear regulators perform and respond to variations in their supplies, loads, and other working conditions
- Work with semiconductor devices--resistors, capacitors, diodes, and transistors
- Combine microelectronic components to design current mirrors, differential pairs, differential amplifiers, linear low-dropout regulators, and their variants
- Close and stabilize feedback control loops that regulate voltages and currents
- Design circuits that establish reliable bias currents and reference circuits
- Determine the small-signal dynamics of analog ICs and analog systems
- Establish independent, stable, noise-free, and predictable power-supply voltages
- Implement overcurrent, thermal, reverse-battery, and ESD protection
- Test, measure, and evaluate linear regulator ICs

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Editorial Review

About the Author

Gabriel Alfonso Rincon-Mora was a Senior Integrated Circuit (IC) Design Team Leader for Texas Instruments from 1994 – 2003. He was appointed an Adjunct Professor at the Georgia Institute of Technology in 1999, and became a full-time faculty member in 2001. Since 2011, Dr. Rincon-Mora has also been a visiting professor at National Cheng Kung University in Taiwan. He has published 140 papers in scientific publications, written eight books, and has more than 26 commercial power chip designs. Dr. Rincon-Mora is an IEEE Fellow "for contributions to energy and power integrated circuit design" and an Institution of Engineering and Technology (IET) Fellow. His research is on the design and development of silicon-based integrated circuits (ICs) that draw and condition power from tiny batteries, fuel cells, and energy-harnessing generators to supply mobile, portable, and self-sustaining devices like wireless microsensors for biomedical, consumer, industrial, and military applications.

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